

40 V, 3 A low VF MEGA Schottky barrier rectifier
Rev. 1 — 10 October 2011 Pro

**Product data sheet** 

#### **Product profile** 1.

### 1.1 General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD128 small and flat lead Surface-Mounted Device (SMD) plastic package.

### 1.2 Features and benefits

- Average forward current: I<sub>F(AV)</sub> ≤ 3 A
- Reverse voltage: V<sub>R</sub> ≤ 40 V
- Low forward voltage
- High power capability due to clip-bonding technology
- Small and flat lead SMD plastic package
- AEC-Q101 qualified
- High temperature T<sub>i</sub> ≤ 175 °C

### 1.3 Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Reverse polarity protection
- Low power consumption applications
- High temperature applications

#### 1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I <sub>F(AV)</sub>	average forward current	square wave; $\delta$ = 0.5; f = 20 kHz; T <sub>amb</sub> ≤ 85 °C	<u>[1]</u>	-	-	3	Α
		square wave; $\delta$ = 0.5; f = 20 kHz; T <sub>sp</sub> ≤ 165 °C		-	-	3	Α
$V_{R}$	reverse voltage	T <sub>j</sub> = 25 °C		-	-	40	V
$V_{F}$	forward voltage	I <sub>F</sub> = 3 A; T <sub>j</sub> = 25 °C		-	430	490	mV
I <sub>R</sub>	reverse current	$T_j = 25  ^{\circ}C;  V_R = 40  V$		-	35	200	μΑ

<sup>[1]</sup> Device mounted on a ceramic Printed-Circuit Board (PCB), Al<sub>2</sub>O<sub>3</sub>, standard footprint.



### 2. Pinning information

#### Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]		. 84 -
2	Α	anode	1 2	1 <del> </del>
			SOD128	

<sup>[1]</sup> The marking bar indicates the cathode.

### 3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMEG4030ETP	-	plastic surface-mounted package; 2 leads	SOD128

### 4. Marking

#### Table 4. Marking codes

Type number	Marking code
PMEG4030ETP	C3

### 5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_R$	reverse voltage	T <sub>j</sub> = 25 °C		-	40	V
I <sub>F(AV)</sub>	average forward current	square wave; $\delta$ = 0.5; f = 20 kHz; T <sub>amb</sub> ≤ 85 °C	<u>[1]</u>	-	3	А
		square wave; $\delta$ = 0.5; f = 20 kHz; T <sub>sp</sub> ≤ 165 °C		-	3	Α
I <sub>FSM</sub>	non-repetitive peak forward current	square wave; $t_p = 8 \text{ ms}$ ; $T_{j(init)} = 25 \text{ °C}$		-	50	Α
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[2][3]	-	750	mW
			[4][3]	-	1250	mW
			[1][3]	-	2500	mW
Tj	junction temperature			-	175	°C
T <sub>amb</sub>	ambient temperature			-55	175	°C
T <sub>stg</sub>	storage temperature			-65	175	°C

<sup>[1]</sup> Device mounted on a ceramic Printed-Circuit Board (PCB), Al<sub>2</sub>O<sub>3</sub>, standard footprint.

### 6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance	in free air	[1][2][3]	-	-	200	K/W
	from junction to ambient		[1][4][3]	-	-	120	K/W
	ambient		[1][5][3]	-	-	60	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		<u>[6]</u>	-	-	12	K/W

<sup>[1]</sup> For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses.

<sup>[2]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

<sup>[3]</sup> Reflow soldering is the only recommended soldering method.

<sup>[4]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

<sup>[2]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

<sup>[3]</sup> Reflow soldering is the only recommended soldering method.

<sup>[4]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

<sup>[5]</sup> Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.

<sup>[6]</sup> Soldering point of cathode tab.

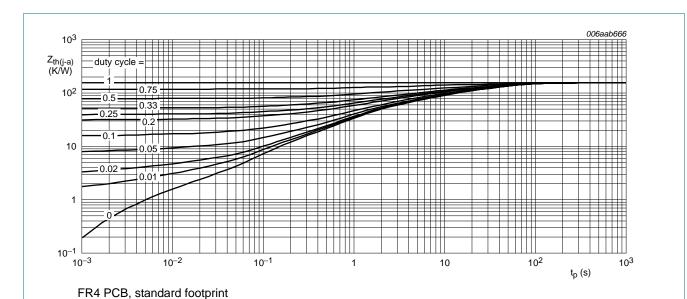


Fig 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

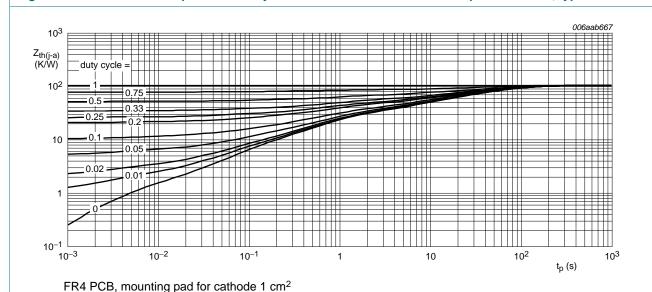
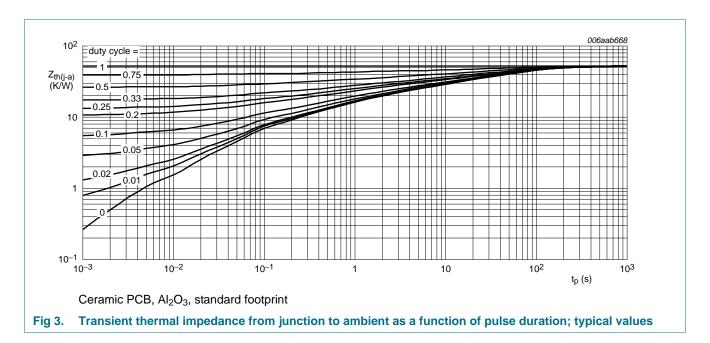


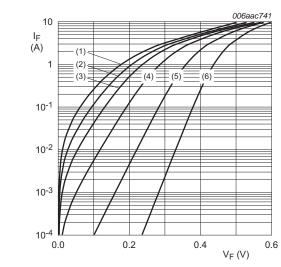
Fig 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



### 7. Characteristics

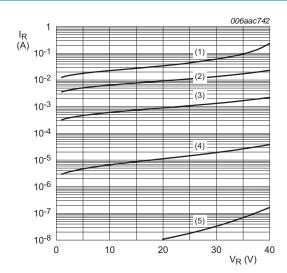
Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>F</sub>	forward voltage	$I_F = 0.1 \text{ A}; T_j = 25 ^{\circ}\text{C}$	-	285	320	mV
		I <sub>F</sub> = 1 A; T <sub>j</sub> = 25 °C	-	360	420	mV
		I <sub>F</sub> = 3 A; T <sub>j</sub> = 25 °C	-	430	490	mV mV μA
		I <sub>F</sub> = 3 A; T <sub>j</sub> = 125 °C	-	330	380	mV
I <sub>R</sub>	reverse current	$V_R = 10 \text{ V}; T_j = 25 ^{\circ}\text{C}$	-	7	-	μΑ
		$V_R = 40 \text{ V}; T_j = 25 ^{\circ}\text{C}$	-	35	200	μΑ
		$V_R = 10 \text{ V}; T_j = 125 ^{\circ}\text{C}$	-	6	-	mA
		$V_R = 40 \text{ V}; T_j = 125 ^{\circ}\text{C}$	-	23	-	mA
C <sub>d</sub>	diode capacitance	$V_R = 1 \text{ V; } f = 1 \text{ MHz; } T_j = 25 ^{\circ}\text{C}$	-	350	-	pF
		$V_R = 10 \text{ V; } f = 1 \text{ MHz; } T_j = 25 \text{ °C}$	-	140	-	pF



- (1)  $T_j = 175 \, ^{\circ}C$
- (2)  $T_j = 150 \, ^{\circ}\text{C}$
- (3)  $T_i = 125 \, ^{\circ}C$
- (4)  $T_j = 85 \, ^{\circ}C$
- (5)  $T_j = 25 \,{}^{\circ}\text{C}$
- (6)  $T_j = -40 \, ^{\circ}C$

Fig 4. Forward current as a function of forward voltage; typical values



- (1)  $T_i = 150 \, ^{\circ}\text{C}$
- (2)  $T_i = 125 \, ^{\circ}C$
- (3)  $T_i = 85 \, ^{\circ}C$
- (4)  $T_j = 25 \, ^{\circ}C$
- (5)  $T_j = -40 \, ^{\circ}\text{C}$

Fig 5. Reverse current as a function of reverse voltage; typical values

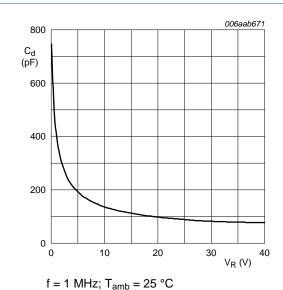
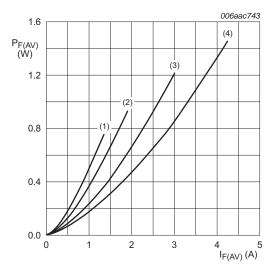
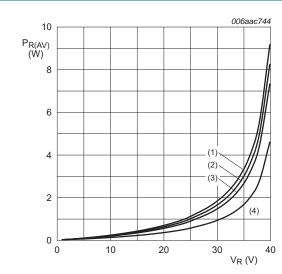


Fig 6. Diode capacitance as a function of reverse voltage; typical values



- $T_i = 175 \,^{\circ}C$
- (1)  $\delta = 0.1$
- (2)  $\delta = 0.2$
- (3)  $\delta = 0.5$
- (4)  $\delta = 1.0$

Fig 7. Average forward power dissipation as a function of average forward current; typical values



T<sub>i</sub> = 150 °C

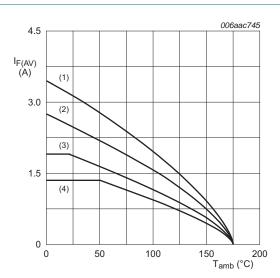
 $(1) \delta = 1.0$ 

(2)  $\delta = 0.9$ 

(3)  $\delta = 0.8$ 

(4)  $\delta = 0.5$ 

Fig 8. Average reverse power dissipation as a function of reverse voltage; typical values



FR4 PCB, standard footprint

T<sub>i</sub> = 175 °C

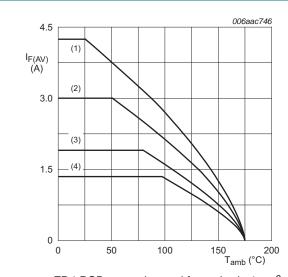
(1)  $\delta = 1.0 (DC)$ 

(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig 9. Average forward current as a function of ambient temperature; typical values



FR4 PCB, mounting pad for cathode 1  $\mbox{cm}^2$ 

T<sub>j</sub> = 175 °C

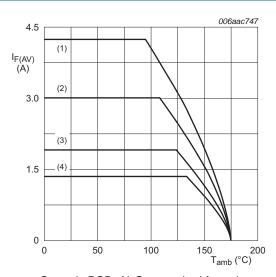
(1)  $\delta = 1.0$ 

(2)  $\delta = 0.9$ 

(3)  $\delta = 0.8$ 

 $(4) \delta = 0.5$ 

Fig 10. Average forward current as a function of ambient temperature; typical values



Ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint

 $T_i = 175 \,{}^{\circ}\text{C}$ 

(1)  $\delta = 1.0 (DC)$ 

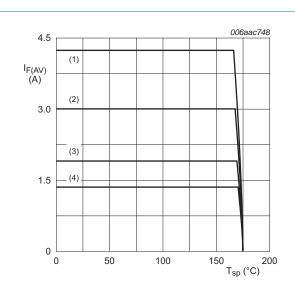
(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig 11. Average forward current as a function of ambient temperature; typical values

PMEG4030ETP



 $T_i = 175$  °C

 $(1) \delta = 1.0$ 

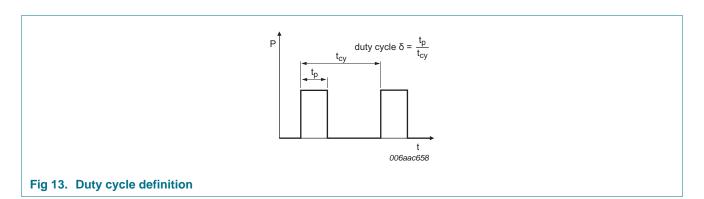
(2)  $\delta = 0.9$ 

(3)  $\delta = 0.8$ 

(4)  $\delta = 0.5$ 

Fig 12. Average forward current as a function of solder point temperature; typical values

### 8. Test information

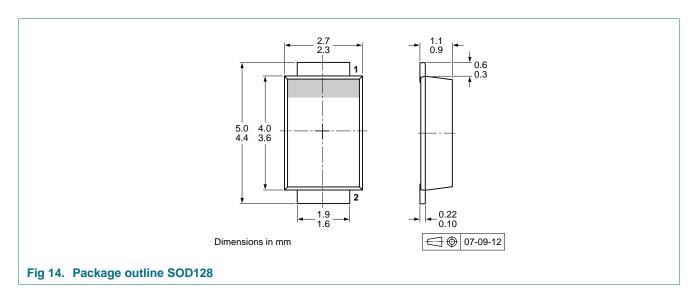


The current ratings for the typical waveforms as shown in figures  $\underline{9}$ ,  $\underline{10}$ ,  $\underline{11}$  and  $\underline{12}$  are calculated according to the equations:  $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current,  $I_{RMS} = I_{F(AV)}$  at DC, and  $I_{RMS} = I_M \times \sqrt{\delta}$  with  $I_{RMS}$  defined as RMS current.

### 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

### 9. Package outline



### 10. Packing information

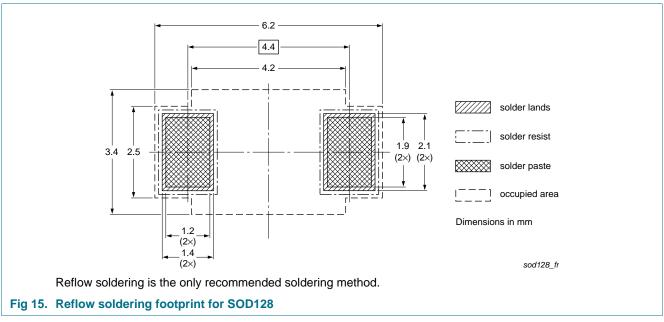
Table 8. Ordering information

The indicated -xxx are the last three digits of the 12NC ordering code. [1]

Type number	Package	Description	Packing quantity
			3000
PMEG4030ETP	SOD128	4 mm pitch, 12 mm tape and reel	-115

[1] For further information and the availability of packing methods, see 14 "Contact information".

### 11. Soldering



PMEG4030ETP

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40 V, 3 A low VF MEGA Schottky barrier rectifier

### 12. Revision history

### Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PMEG4030ETP v.1	20111010	Product data sheet	-	-

### 13. Legal information

#### 13.1 Data sheet status

Document status [1] [2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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#### 40 V, 3 A low VF MEGA Schottky barrier rectifier

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### 40 V, 3 A low VF MEGA Schottky barrier rectifier

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